Programs: Electrical & Computer Engineering

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| --- | --- |
| Course Number | **COE608** |
| Course Title | **Computer Architecture** |
| Semester/Year | **Winter 2019** |
| Instructor | **Naimul Khan** |
| FLC | **Alagan Anpalagan** |

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| --- | --- |
| **Lab Report No.** | **2** |

|  |  |
| --- | --- |
| Report Title | **Program Counter and Register Set Design** |

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| --- | --- |
| Section No. | **04** |
| Group No. |  |
| Submission Date | **31/01/19** |
| Due Date | **31/01/19** |

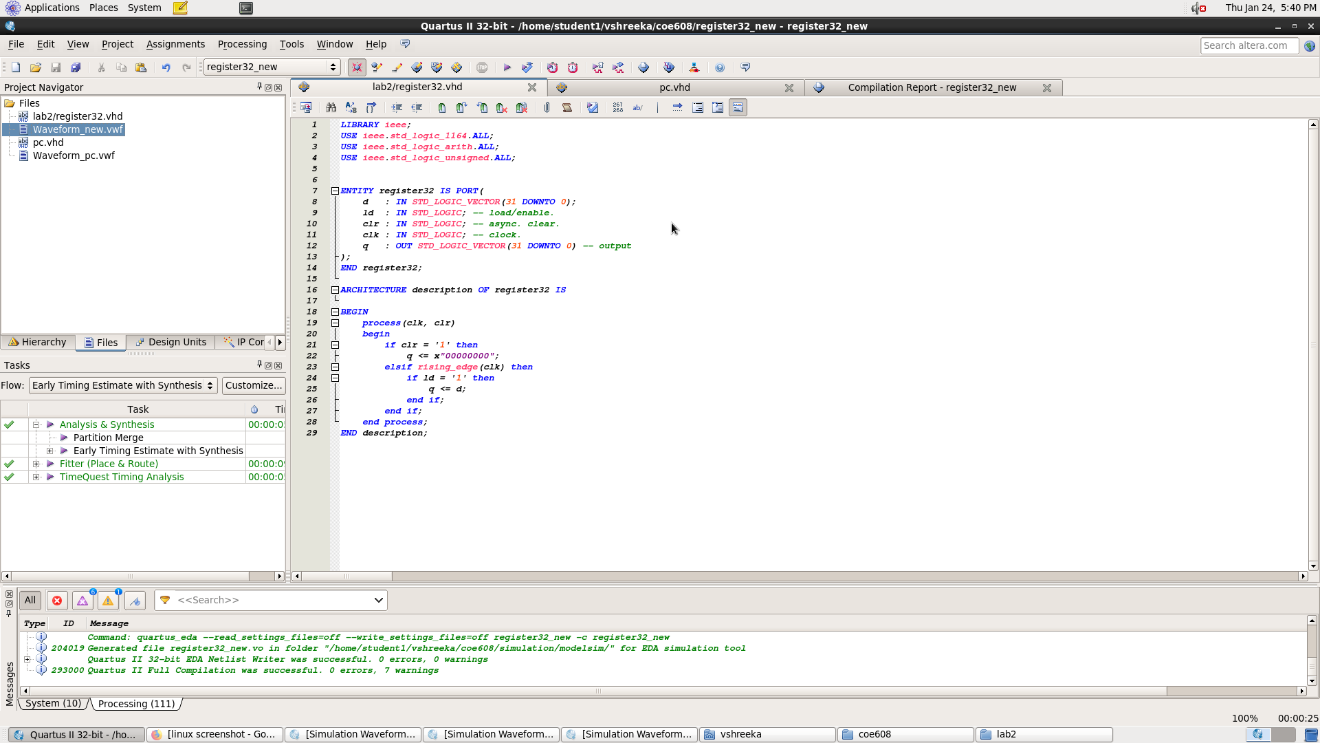
|  |  |  |
| --- | --- | --- |
| Name | Student ID | Signature\* |
| Vatsal Shreekant | 71363 |  |

(Note: remove the first 4 digits from your student ID)

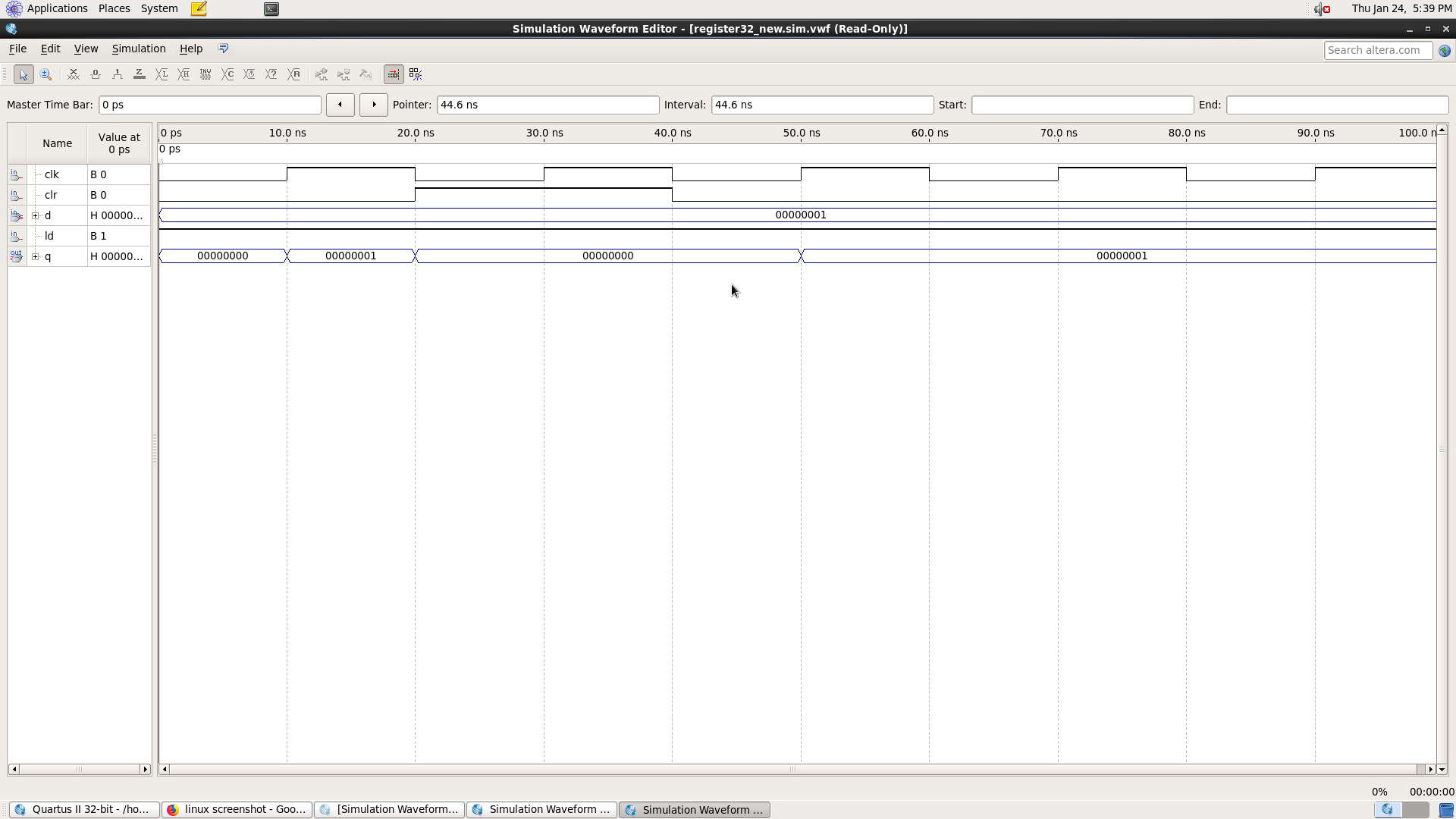
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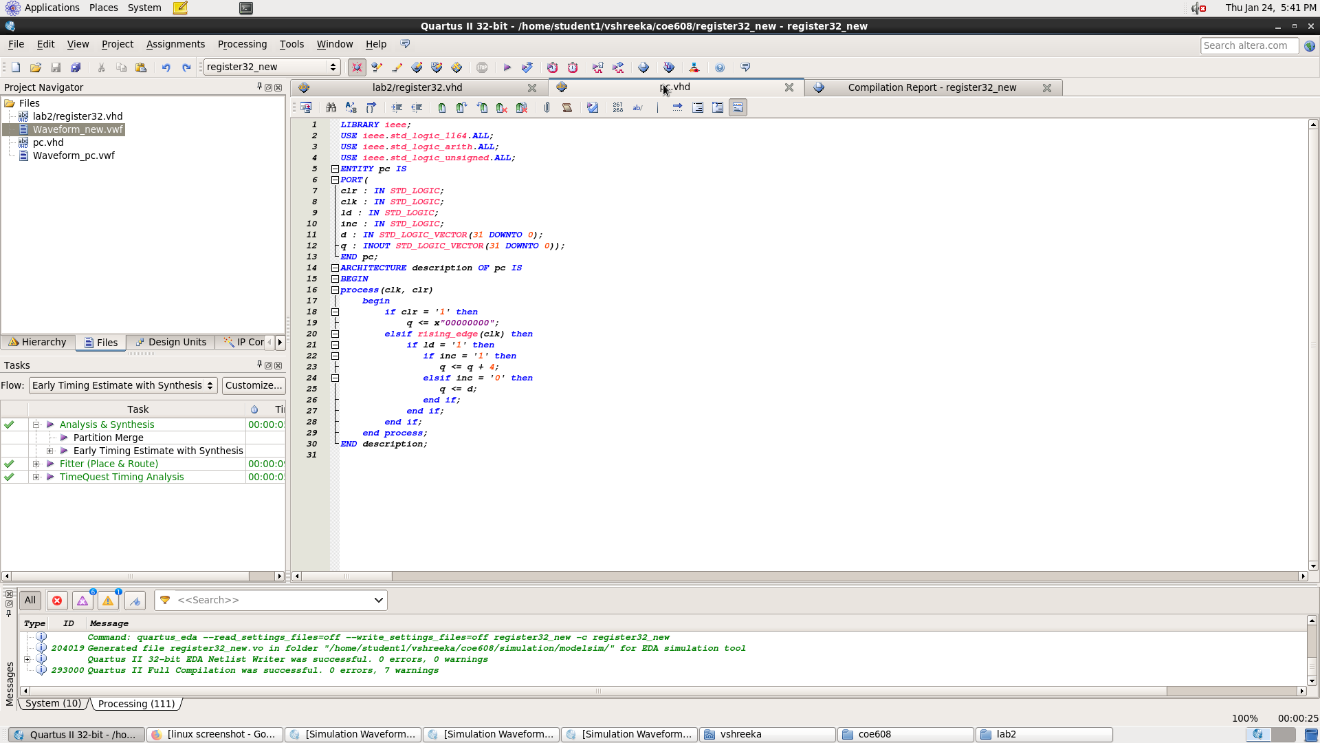
***Register 32 bit code:***



***Waveform for Register 32 bit:***



***Program Counter code:***



***Waveform for Program Counter:***

